

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: HWANG, Chang-Won and CHUNG, Woo-Suk
Assignee: SAMSUNG ELECTRONICS CO., LTD.
Title: THIN FILM TRANSISTOR ARRAY PANEL AND
MANUFACTURING METHOD THEREOF
Application No.: 10/534,785 Filing Date: 05/13/2005
Int'l. Appln No.: PCT/KR2003/002460 Int'l. Filing Date: 11/14/2003
Examiner: Crane, Sara W. Group Art Unit: 2811
Docket No.: AB-1434 US Confirmation No.: 1780

San Jose, California
April 25, 2007

Mail Stop Amendment
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

Dear Sir:

In response to the Restriction Requirement having a mailing date of March 26, 2007, Applicant hereby elects the claims of Group I (Claims 1-15, drawn to a semiconductor device, classified in class 257, subclass 72) for further prosecution. Applicant withdraws the non-elected claims of Group II (Claims 16-20, drawn to a method of making a semiconductor device, classified in class 438, subclass 22+) reserving the right to prosecute those claims in another application.

The Examiner is invited to call the undersigned at (408) 392-9250 with any questions regarding the above-identified application.

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The Commissioner is authorized to charge any underpayment or credit any overpayment to Deposit Account No. 50-2257 for any matter in connection with this response, including any fee for extension of time and/or fee for additional claims, which may be required.

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Certificate of Transmission: I hereby certify that this correspondence is being transmitted to the United States Patent and Trademark Office (USPTO) via the USPTO's electronic filing system on the Date of Signature indicated below.	
<u>H. Matsubayashi</u> _____ Attorney for Applicant(s)	<u>4/25/2007</u> _____ Date of Signature

Respectfully submitted,



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